


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<p>Applicant(s): Dupuis</p> <p>Application No.: 10/673,750</p> <p>Filed: September 29, 2003</p> <p>Title: METHOD AND APPARATUS FOR PROTECTING DEVICES IN AN RF POWER AMPLIFIER</p> <p>Attorney Docket No.: SIL.P0066</p>	 <p>Group Art Unit: 2817</p> <p>Examiner: SHINGLETON, MICHAEL B</p>
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SECTION 1.131 DECLARATION OF BRUCE A. JOHNSON

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

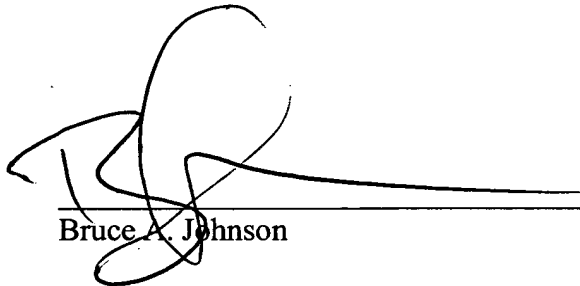
I, Bruce A. Johnson, hereby declare the following:

1. I am the attorney of record of patent application Serial Number 10/673,750 filed on September 29, 2003, which is a continuation patent application of Serial Number 09/932,817 filed on August 17, 2001.
2. I understand that the Examiner has cited Denning et al. US Patent 6,525,611 issued on February 25, 2003 and filed on August 1, 2001.
3. I am the attorney that prepared patent application Serial Number 09/932,817 filed on August 17, 2001.
4. The inventor, Tim Dupuis, was in possession of the subject matter of the invention set forth in the patent applications listed in paragraph 1 earlier than the August 1, 2001 filing date of Denning et al. Attached as evidence is a print out of an email (2 pages), showing that I sent a copy of the drafted patent application to Tim Dupuis on July 31, 2001.

5. From the time prior to August 1, 2001 to the filing date of August 17, 2001 (13 working days), I worked dilligently to complete the patent application that was filed on August 17, 2001 and assigned Serial Number Number 09/932,817. This work included revising and finalizing the patent application.

6. I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

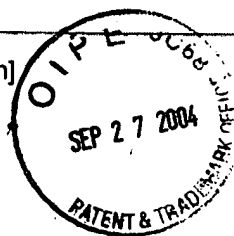
9-23-04
Date



Bruce A. Johnson

Bruce A. Johnson

From: tj@silabs.com on behalf of Tim Dupuis [tjd@silabs.com]
Sent: Wednesday, August 15, 2001 10:48 AM
To: Bruce A. Johnson
Subject: Re: FW: Power Control Patent



Hi Bruce,

I went over the revised patent application. Sorry it took awhile but I was in Europe last week.

It looks very good. There are only a couple minor typos I found.

Pg. 6 line 12 :
GSM,CDMA,PDS,DCS, etc.

PDS should be PCS.

Pg. 9 line 13
The sentence
The power control circuitry 116 may implement the algorithm using analog or digital signal processing.
should be removed, it's basically repeated in the next sentence.

Other than that, it looks great. I'm the only inventor on this, so I can sign it and we can get it submitted as soon as you are ready.

Take Care
Tim

"Bruce A. Johnson" wrote:

Here are the attachments...

-----Original Message-----

From: Bruce A. Johnson [mailto:bjohnson@capital-ip.com]

Sent: Tuesday, July 31, 2001 4:17 PM

To: tj@silabs.com

Subject: RE: Power Control Patent

Tim, Attached are 2 encrypted PDF files containing a revised Specification revised drawings. Please review the draft again and let me know if you have any other comments or suggestions. Also, please confirm that you are the only inventor. Thanks!

Bruce=====

Bruce A. Johnson

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8/16/01

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-----Original Message-----

From: tjd@silabs.com [<mailto:tjd@silabs.com>]

Sent: Friday, July 27, 2001 9:00 AM

To: bjohnson@capital-ip.com

Subject: Power Control Patent

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Tim Dupuis

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8/16/01